

Resume Herman van Twillert part 2

focused on Electronic- and Semiconductor Industries

Work Experience with ATE / RF-microwave relevance:

- Test development support at INTEL Ireland (leixlip) (ATE) June 2016 till juli 2017
- Advantest Germany. Application Engineer . (ATE) Nov 2015 - June 2016
- Test development support at INTEL Mobile Comms. (ATE) June 2012 - 2015
- Test development Engineer, Van Limburg Eng BV. (ATE) January 2011 - 2012
- Technical Director, Van Limburg Engineering BV. (ATE, IT) September 1999 – 2011
- Teacher at Verigy) (ATE test systems) 2002 - 2008
- Teacher Technical University Windesheim (nl) (Electronics science package) 2004
- Free Lance Application Engineer (Food-process and Packaging systems) 1997 – 1999
- General Director. PM Systems BV (Processing systems) 1989 - 1997
- Technical Director. PM Soft-Electronic Systems (Automation, Processing) 1985 – 1988
- Avionics Test Engineer. Royal Dutch Air force (ATE) 1980 - 1985
- Radar special systems Engineer. Royal Dutch Air force (μwave technology) 1974 - 1980
- Test Engineer. Control Data Corporation (Data processing) 1970 - 1971

Education:

- Dr.A.F.Philipsschool for Telecommunications, Electronics 1967 - 1969
- University RENS&RENS for electronics 1969 – 1970
- NTS. Dutch Technical School for advanced electronics 1970 – 1971

Equivalent to: *Master of Science in Electrical Engineering*

Courses:

- Royal Dutch Airforce KLU 1971 - 1974
 - Impuls Electronics*
 - High Frequency techniques*
 - Line- and Microwave technology*
 - Pulse radar system*
 - Micro-processor design and engineering*
- Royal Belgium Air force 1974 – 1975
 - HAWK Radar Specialist*
- USA Air force USAMS Huntsville (AL) 1977
 - Guided Missiles Pulse Radar Systems*
- General Dynamics Fort Worth(TX) 1980
 - F16 Avionics ATE on Inertial Navigation systems/ Flight Control Computer systems*
- General Dynamics Fort Worth(TX) 1984
 - HP-1000 Control and Support software development/maintenance*
- Teradyne October 1999
 - A585 Digital end Mixed Signal Test development*
- Teradyne September 2001
 - Catalyst Digital, Mixed Signal and Microwave RF Test program engineering*
- Agilent (Verigy) August 2002
 - HP93000 SOC tester Part 1 Digital testing*
- Agilent(Verigy) April 2004
 - HP93000 SOC tester Mixed Signal*
- Test Advantage April 2007
 - Expert Statistical Test Data Processing*
- Verigy August 2008
 - V93000 SOC Port Scale Microwave-RF Users training SOC MS*
- Verigy March 2009
 - V93000 SOC tester Service Training*

Educational Highlines:

- Digital Logic; Digital Circuit
- Computer Systems Organization
- Programming Systems Design
- Computer Networks Win2000server
- TCP/IP networks, Web-servers
- Communication Systems; Linear Systems
- Computer Operating System Development
- PLC oriented design and program development
- Mixed Signal Electronic Circuits
- Linear Circuits
- RF Microwave engineering
- Physical Electronics; Electronic Circuits
- Electromagnetics
- Technical Entrepreneurship
- Teaching capabilities

Skills:

Test Program Application: Catalyst, A560/A580 IMAGE (6.4/7.0 including RF-uWave option), Verigy Smartest, General Dynamics Avionics test language ATLAS

Programming Languages: C, C++, ATLAS, Assembly languages, HTML, CGI, perl

Software Applications: Spice3, Protel, Orcad, Eagle, Electronic Workbench, X-winPro, Telnet, MS Office,

Operating Systems: HP-RTEIII, UNIX, HP_Unix, Linux, Solaris OS, DOS, Windows (3.1/95/98/NT/2000-server/XP) Worldwide System administrator for VLE

Some Projects Experiences:

- **INTEL IOT Ireland on Advantest 93k Systems**
Test program hardware- software design and development on Intel IoT controller/processors with focus on Digital and Flash Memory NVM .
As a consultant the works involves support on V93k digital, mixed signal
- **ADVANTEST Germany on Advantest- 93k Systems**
Test program hardware- software design As a consultant the works involves support on V93k digital to Advantest customers
- **INTEL Mobile Communications Germany on Advantest 93k Systems**
Test program hardware- software design and development on Intel Mobile Communication devices as LTE/4G Transceivers with focus on Digital and RF testing.
As a consultant the works involves support on V93k digital, mixed signal and RF testing within the development group.
- **INTEL Mobile Communications Germany on Advantest/Verigy-93k Systems**
Test program probing-board hardware- and software design and development/improvement on Memory Testchip. A vehicle for a wide range of memory modules and tools. Test program is adapted for characterisation and qualifying the tests itself. As a consultant the works involves support on V93k digital testing, advising and aligning the development group.
- **COGNEX USA/IRELAND on Verigy /93000 Systems**
Test program and load-board hardware- and software design and development from scratch for the Cognex Image Processor. Tester installation and program environment specification and qualification at VLE office Works at VLE office for 9 months after which the tester moved to Customer premises Ireland
- **AMD DresdenGermany HP93000 Systems**
Implementation and customer teaching of AMD test Engineers on the Verigy 93000 digital tester.
On site at **AMD Dresden**

- *Xx xx Germany / HP93000 Systems*
Development and implementation of CVS system in conjunction with available Verigy Pinscale Tester Works at **xx Germany** Freysing for 6 months
- *Xx xx USA / Teradyne A580 Systems*
Implementation and conversions of global TI User Libraries throughout Worldwide Teradyne Test Programs Works at site **xx Houston(TX)** over a period of 1 year
- *Zxx Dresden conversions from HP9400 (analog) to HP93000 MX Systems*
Implementation and conversions of the ZMD analog devices for hearing aids.
Testprogram for replacement of the aged HP9400 analog tester to the Verigy 93000 Mixed Signal tester.
Teached Zxx engineers in program development
- *Xx/xx on Verigy /93000 Systems*
Development of PL@T user Library (cookbook) design and development of test-innovation strategies and techniques.
Works achieved at Nijmegen site
- *Xx/xx on Verigy /93000 Systems*
Test program development and structuring of Automotive electronic devices (Car-Radio, -Navigation, Car management)
- *LTX USA*
Presentations and teaching of test-pattern development and conversion techniques on site at LTX at Westwood (ma) and LTX Milpitas(ca)
- *HTS Windesheim Technical University at Zwolle NL*
Lecturing, tutoring and coaching bachelor students in Electrical Engineering of 1st, 2nd and final grade.
Courses are basic- and advanced electronics, digital electronics, mechatronics, FPGA programming.
- *Xx NL, Conversions from Teradyne to Agilent 83k/93k Systems*
Converting, extending and advancing a series of Car-radio devices from 1 site Catalyst to Verigy93k multi site.
Specialism: developed new methodology for IDDQ measurements techniques and developing techniques to detect GateDelay defects. Including load-board hardware.
Works partly at xxNL Nijmegen during the conversion to xxNijmegen for a duration of 3 years
- *AGILENT / VERIGY*
Since 2002 Fully Qualified teacher on AGILENT and VERIGY 83000 / 93000 SOC systems
Teaching Digital and Mixed Signal test methodology and techniques.
Weekly raining sessions over a period of 6 years at Verigy Germany Böblingen
- *ST-France, Conversions from Schlumberger ITS to Agilent 83k/93k Systems*
Converting a series of Flash memory and MCD device test from ITS system to the Agilent platforms. Converted number of microprocessor devices.
Works at ST- France site Rousset for 18 months.
- *Xx NL, Research on Ozone-generation utilizing microwave techniques.*
Research and development of a method were microwave energy and high-voltage is used to initiate plasma and generating Ozone for food-processing.
- *ATMEL France, Training package development for users of the Terayne A560 CCD Testhead.*
Customer teaching and presentations on-site at ATMEL France at site Grenoble.

- *Teradyne Paris/Grenoble, Extending Teradyne A560 with Featured Testhead with custom CCD devices*
Design and development of a complete set of drivers extending the A560 with 2 extra test heads enabling the system to fully test CCD devices. Each test head comprised to 64 3-level programmable digital channels, 8 Video channels , 5 Pico-current amplifiers and a central CDS measuring system.
Software covers full set of low-level drivers and high-language extensions of the instruction-set of Image6.4 with a range of very powerful and dedicated CCD configuration and measuring functions.
Complete set of software tools as Checkers, Calibration, Self-test, Performance, System-configuration and a range of program-development features.
Works at site Teradyne at Paris and Grenoble (18months)
- *CENTOCOR Pharmaceutical NL, u-Processor controller.*
Design, development and full realization of hardware (sbc) en control and support software (operating) system to operate a complex of pharmaceutical systems to allow free application programming for the technical engineering/management.
- *Royal Dutch Airforce NL, Avionics Test Development ATE*
Development and maintenance of Automatic F16 Avionics Module testing
- *80xx/Z80 Computer System.*
designed and manufactured a single-board-system utilizing the 8052 microprocessor, EPROM and applications in numerous applications in the food processing- and packaging industry.
- *Processing Application.*
Hardware and Software design and development for complete food-processing plant involving MIS, MMS, Production- and Formula Database development, Hardware to Control- and Management connection as well as all programming for all the machine controlling (weighing, storing, processing, routing, packaging), information capturing and production control.
- *Modem Technologies*
Designed embedded software transceiver system and operating system on the SP800series computers, enabling full-duplex communications on the European Viditel systems

xxxx with respect to NDA compliance some customer names are masked

Other Personal Skills:

Fluent in Dutch, English, German and some French.